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<u>REMARKS</u>

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Herein, the "Action" or "Office Action" refers to the Office Action dated 11/30/2005.

Applicant respectfully requests reconsideration and allowance of all of the claims of the application. Claims 1-42 and 45-46 are presently pending. Claims amended herein are 10 and 29. Claims withdrawn or cancelled herein are 43-44. New claims added herein are none.

Election/Restriction

In the Action, the Office states:

Election/Restrictions

- Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - Claims 1-42,45-46, drawn to a kernel emulator comprising translating instructions, addresses, and arguments from non-native to native code, classified in class 703, subclass 23.
 - II. Claims 43-44, drawn to a termel emulator comprising a target platform determiner, an instruction type detector, a translator selector, classified in class 703, subclass 23.
- 4. Inventions I and II are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, invention II has separate utility such as determining the target platform, determining the type of non-native instructions, and selecting a translator capable of translating the instructions. See MPEP § 806.05(d).
- 5. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art because of their recognized divergent subject matter, restriction for examination purposes as indicated is proper.
- 6. During a telephone conversation with Mr. Kasey Christie on 11/11/04 a provisional election was made without traverse to prosecute the invention of Group I, Claims 1-42, 45-46. Affirmation of this election must be made by applicant in replying to this Office action. Claims 43-44 withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

Applicant confirms the election of claims 1-42 and 45-46. Accordingly, Applicant withdraws claims 43 and 44 from consideration here.

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Informalities

Specification

The Office indicates that lines 16 of page 23 should read "within" instead of "with in." Applicant amends herein to correct accordingly.

Claim Objections

The Office asks for small informalities to claims 10 and 42 to be corrected. Applicant amends herein to correct accordingly.

Claim Interpretation

The Office indicates:

Claim Interpretation

Claims 9 and 29, recite a "shared-memory manager configured to synchronize a native shared data structure with a non-native shared data structure". It was unclear as to what "synchronize" meant. The specification states, "The address conversion (between native and non-native addressing formats) is done during synchronization" (page 23, lines 13-15). From this statement in the specification, it was determined that synchronization is the address conversion of a non-native address to a native address.

Applicant submits that "synchronize" as used in these claims (claims 9 and 29) does indeed include address conversion. However, synchronization is not limited to just address conversion. It may include other data format conversions.

The two data structures are "synchronized" when the data in one of the data structures is accessible in the other data structure. This includes, for example, address conversion, instruction conversion, argument-format conversion, wordsize conversion, and other conversions described in the Application and other similar conversions consistent with a native to/from non-native conversion.

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Substantive Claim Rejections

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Claim Rejections under §§ 102 and 103

The Office rejects all of the pending claims (1-42 and 45-46) under §102 and/or §103. For the reasons set forth below, the Office has not shown that one or more of the cited references anticipate the rejected claims. For the reasons set forth below, the Office has not made a prima facia case showing that the rejected claims are obvious (under §103). Accordingly, Applicant respectfully requests that the rejections be withdrawn and the case be passed along to issuance.

The Office's rejections are based upon the following references:

- Scalzi: Scalzi et al., US Patent No. 5,560,013 (issued 9/24/1996);
- Franz: Michael Franz, "Emulating an Operating System on Top of Another" Software - Practice and Experience. Vol. 23, No. 6, June 1993, pp. 677-692);
- **Duvall:** Duvall et al., US Patent No. 4,742,447 (issued 5/3/1988);
- McCoy: McCoy et al., US Patent No. 5,036,484 (issued 7/30/1991).

Overview of the Application

The Application describes a technology facilitating the operation of nonnative program modules within a native computing platform. More particularly, it describes a technology facilitating the interoperability of native and non-native program modules within a native computing platform.

Specifically, this technology involves an emulation of the kernel of the nonnative operating system. Instead of interacting with the native kernel of the native computing platform, the non-native program modules interact with a non-native

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kernel emulator. This emulator handles the necessary conversions and translations. With this non-native kernel emulation, native and non-native program modules are interoperable. Except for the kernel emulator, none of the program module (native or non-native) and none of the other portions of the native computing platform are aware of the emulation. The computing environment and other program modules appear to be non-native to the non-native program modules. Likewise, the non-native program modules appear to be native to the computing environment and the native program modules.

Cited References

The Office cites Scalzi as its reference for is anticipation-based rejections and the primary references in many of its obviousness-based rejections. The Office cites Duvall as its primary reference for some of its obviousness rejections and a secondary reference in other obviousness-based rejections. In addition, the Office cites Franz and McCoy as secondary references in some of its obviousness-based rejections.

<u>Scalzi</u>

Scalzi describes a method of utilizing large virtual addressing in a target computer to implement an instruction set translator (IST) for dynamically translating the machine language instructions of an alien source computer into a set of functionally equivalent target computer machine language instructions, providing in the target machine, an execution environment for source machine operating systems, application subsystems, and applications.

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The target system provides a unique pointer table in target virtual address space that connects each source program instruction in the multiple source virtual address spaces to a target instruction translation which emulates the function of that source instruction in the target system. The target system stores the translated executable source programs by actually storing only one copy of any source program, regardless of the number of source address spaces in which the source program exists.

The target system manages dynamic changes in the source machine storage, accommodating the nature of a preemptive, multitasking source operating system. The target system preserves the security and data integrity for the source programs on a par with their security and data integrity obtainable when executing in source processors (i.e. having the source architecture as their native architecture). The target computer execution maintains source-architected logical separations between programs and data executing in different source address spaces—without a need for the target system to be aware of the source virtual address spaces.

Duvall

Duvall describes a method for accessing information in a page segmented virtual memory data processing system in which virtual machines running UNIX type operating systems are concurrently established, and in which a memory manager controls the transfer of information between primary and secondary storage devices in response to the occurrence of page faults. The method establishes a plurality of data structures in a dynamic manner in response to a Supervisor call to "map" a file.

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The mapping process assigns a new segment of virtual memory to the mapped file and correlates, in one data structure, the virtual address of each page of data in the new segment to a disk file address where that page is actually stored. A UNIX system call by an application program for a specific virtual page is handled by the page fault hanger, and not the UNIX kernel, since the application can supply the real address of the page on the disk file from the data structure that was created by the mapped page range Supervisor call. Simple load and store type of instructions are employed for the data transfer, which avoids much of the overhead that normally accompanies conventional UNIX read and write system calls to the storage subsystem.

<u>Franz</u>

As its name implies, Franz discusses the emulation of one operating system on top of another operating system. Franz describes the design of an operating-system emulator. This software interface provides the services of one operating system (e.g., Oberon) on a machine running a different operating system (e.g., Macintosh), by mapping the functions of the first onto equivalent calls to the second.

McCoy

McCoy describes a system for emulating the operation of a terminal connected to a host computing system while retaining the ability to utilize personal computer application programs resident in the personal computer by utilizing a

personal computer/host terminal emulation program which conducts an analysis of host data and keystrokes to identify personal computer commands and calls the appropriate resident application program in response to such commands.

Anticipation Rejections

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Anticipation Rejections Based upon Scalzi

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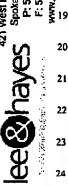
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The Office rejects claims 1, 3-6, 9-13, 15-17, 19-28, 34, 37-40, 42, and 45-46 under USC § 102(b) as being anticipated by Scalzi. Applicant respectfully traverses the rejections of these claims. Based on the reasons given below, Applicant asks the Office to withdraw its rejections of these claims.

Claims 1, 3, 4, 13, 15, 16, 34, 40, and 45

In the Action, the Office provides the following reasoning for rejecting these claims:

- 14. Claims 1,3-6,9-13,15-17,19-28, 34,37-40,42,45-46 are rejected under 35 U.S.C. 102(b) as being anticipated by Scalzi et al (U.S. Patent Number 5,560,013), herein referred to as Scalzi.
- 15. As to Claims 1,3,4,13,15,16,34, 40 and 45, Scalzi teaches: a kernel emulator for non-native program modules, the emulator comprising: an interceptor configured to intercept kernel calls from non-native program modules (Figure 1, element 102 and description); a call-converter configured to convert non-native kernel calls intercepted by the interceptor into native kernel calls (Figure 1, element 103 and description); wherein the call-converter comprises: an instruction-translator configured to translate non-native CPU instructions into native CPU instructions (Figure 1, element 103 and description); an address-translator configured to translate addresses from non-native length into native length (Figure 3 and description, column 21, lines 42-48).

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All of these rejected claims recite kernel emulation¹ and operating on or in response to kernel calls. Scalzi never discloses a call to a kernel of an operating system. Furthermore, Scalzi never discloses emulating a kernel of an operating system. Further still, Scalzi never even mentions a kernel of an operating system.

Moreover, many of these claims² recite an interception of a kernel call. Since Scalzi never mentions kernel calls, it is logical that is never discloses intercepting such calls.

While Scalzi does appear to disclose instructions conversion, it fails to disclose kernel emulation, kernel calls, and interception of such kernel calls. As shown above, Scalzi does not disclose all of the claimed elements and features of these claims. Accordingly, Applicant asks the Office to withdraw its rejection of these claims.

Claims 2-13

These claims ultimately depend upon independent claim 1. As discussed above, claim 1 is allowable.

¹ E.g., "kernel emulator" in claims 1, 40 and 45, "emulating a kernel" in claim 13, and "emulating a non-native kernel" in claim 34

² E.g., independent claims 1, 13, and 45

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Claims 14-28

These claims ultimately depend upon independent claim 13. As discussed above, claim 13 is allowable.

In addition to its own merits, each of these dependent claims is allowable for the same reasons that its base claim is allowable. Applicant submits that the Office withdraw the rejection of each of these dependent claims because its base claim is allowable.

Claims 35-39

These claims ultimately depend upon independent claim 34. As discussed above, claim 34 is allowable.

In addition to its own merits, each of these dependent claims is allowable for the same reasons that its base claim is allowable. Applicant submits that the Office withdraw the rejection of each of these dependent claims because its base claim is allowable.

Claims 41 and 42

These claims ultimately depend upon independent claim 40. As discussed above, claim 40 is allowable.

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Claim 46

This claim ultimately depends upon independent claim 45. As discussed above, claim 45 is allowable.

In addition to its own merits, this dependent claim is allowable for the same reasons that its base claim is allowable. Applicant submits that the Office withdraw the rejection of this dependent claim because its base claim is allowable.

Obviousness Rejections

Lack of Prima Facie Case of Obviousness (MPEP § 2142)

Applicant disagrees with the Office's obviousness rejections. Arguments presented herein point to various aspects of the record to demonstrate that all of the criteria set forth for making a *prima facie* case have not been met.

Based upon Scalzi and Franz

The Office rejects claims 2 and 14 under USC § 103(a) as being unpatentable over Scalzi in view of Franz. Applicant respectfully traverses the rejection of this claim. Applicant asks the Office to withdraw its rejection of this claim.

Claims 2 and 14

Claim 2 ultimately depends upon independent claim 1. As discussed above, claim 1 is allowable. Claim 14 ultimately depends upon independent claim 13. As discussed above, claim 13 is allowable.

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In addition to its own merits, each of these dependent claims is allowable for the same reasons that its base claim is allowable. Applicant submits that the Office withdraw the rejection of each of these dependent claims because its base claim is allowable.

Based upon Scalzi and Duvall

The Office rejects claims 7, 8, 18, 35, and 41 under USC § 103(a) as being unpatentable over Scalzi in view of Duvall. Applicant respectfully traverses the rejection of this claim. Applicant asks the Office to withdraw its rejection of this claim.

Claims 7, 8, 18, 35, and 41

Claims 7 and 8 ultimately depend upon independent claim 1. As discussed above, claim 1 is allowable. Claim 18 ultimately depends upon independent claim 13. As discussed above, claim 13 is allowable. Claim 35 ultimately depends upon independent claim 34. As discussed above, claim 34 is allowable. Claim 41 ultimately depends upon independent claim 40. As discussed above, claim 40 is allowable.

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Based upon Duyall and McCoy

The Office rejects claims 29-33 under USC § 103(a) as being unpatentable over Duvall in view of McCoy. Applicant respectfully traverses the rejection of this claim. Applicant asks the Office to withdraw its rejection of these claims.

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Claim 29

In the Action, the Office provides the following reasoning for rejecting this claim:

- Claims 29-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Davall and further 41. in view of McCoy et al (U.S. Patent Number 5,036,484), herein referred to as McCoy.
- As to Claim 29, Duvall teaches: a method comprising: if the initiating program is non-native: 42. limiting available memory to a range that is addressable by the non-native program module (column 4, lines 43-46, column 6, lines 25-29, column 9, lines 20-25); establishing non-native a version of a shared memory data structure that may be synchronized with a native version of the same shared memory data structure (column 5, lines 45-51, column 6, lines 25-29).
- Duvall further traches the data in a segment of virtual memory is created as a result of an application program being run (column 5, lines 52-55). While this implies that must be some determination as to weather a program is native or non-native allowing for the segment in virtual memory to be created, Duvall does not expressly teach determining whether an initiating program module is a native or non-native.
- McCoy traches determining whether an initiating program module is a native or non-native 44. (Figure 3a, element 36a, column 5, lines 40-48) in a system that emulates a host program in a PC environment and translates host data to PC format by the emulation program (column 5, lines 28-31), allowing the system to know whether to perform a function of the native system or perform a function of the non-native system which includes the translation of code (Figure 3a, element 31a and column 5, lines 40-48) since in the emulation systems of the prior art, when operating in emulation mode, the native system is incapable of performing functions other than those of the terminal which is being conslated.

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It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the determination of whether a program module is native or non-native as taught in **Duval** with the method of determining whether a program module is native or non-native as taught by McCoy since McCoy teaches that typically, when operating in simulation mode, the native system is incapable of performing functions other than those of the terminal which is being simulated (column 1, lines 32-39) and his method provides a way of switching between running native and non-native functions. Further, both Duvall and McCoy are directed to the emulation of a non-native program module in a native environment and translating the non-native data into native data.

Duvall discloses virtual machine (VM) technology, which the Applicant discusses in its Background section on p. 7 and 8 of the Application. Duvall discloses a new addressing scheme for VMs to use to read/write from/to a "file" (rather than memory). Duvall does not disclose "limiting available memory to a range that is addressable." Rather, it discloses a re-definition and re-arrangement of the meaning of the bits in the existing and unmodified addressable range.

To make it clear that the "limiting" has the effect of reducing the range of available memory that a non-native program module may address, Applicant amends herein this claim in this manner:

limiting available memory to a range that is addressable by the non-native program module, that range of addressable memory being less that the available memory

Applicant submits that this amendment does not narrow the claim. Rather is clarifies its original meaning.

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Moreover, the Office has not identified where **Duvall** discloses "non-native" program modules. Indeed, since **Duvall** discloses a VM model, then all program modules operating under a particular VM are presumptively native to that VM. If not, then an emulator would be necessary, but **Duvall** does not disclose an emulator.

While McCoy does disclose a nominal "emulator," it is not an emulation related to program modules being considered native or non-native. Rather, McCoy discloses a terminal emulation—that is, emulation of the operation of a "dumb" terminal connected to a host computer (e.g., mainframe computer).

The Office indicates the McCoy discloses an initiation of a program module based upon a determination of whether a program is native or non-native. It points to col. 5, lines 40-48, Fig. 3a, element 36a, which is reproduced here:

Keystrokes on the keyboard/display 35a are examined by the keystroke interpretation portion 36a of the emulation program to determine whether a PC or a host function is required. Program block 36a is responsive to the selected mode. In the PC mode, the keystrokes are handled by block 37a as normal keyboard commands or data. In the emulation mode, the keystrokes representing the host keys are passed to the host processor via the host emulator 31a.

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However, this particular cited portion (and McCoy as a whole) are focused on determining from whence input (e.g., keystrokes) is received and processing them accordingly. The first sentence of the passage above says, "Keystrokes...are examined...to determine whether a PC or a host function is required." Applicant respectfully submits that this is not equivalent to "determining whether an initiating program module is a native or non-native."

Indeed, Applicant submits that all of McCoy's program modules (include the McCoy's terminal emulation program itself) are presumptively native. If they were non-native, then they would not function on the PC absent an operating-system based emulation program. However, McCoy does not disclose a such an emulation program.

For the reasons given above, Applicant submits the combination of **Duvall** and **McCoy** fail to disclose all of the elements and features of this claim. Accordingly, Applicant asks the Office to withdraw its rejection of this claim.

No Motivation to Combine References

Furthermore, Applicant asserts that there is no motivation to combine the teachings of **Duvall** and the teachings of **McCoy**.

As discussed above, **Duvall** describes an addressing scheme for accessing files in a VM environment. However, **McCoy** describes "dumb" terminal emulation on a PC.

Applicant submits that there is no suggestion, teaching, or reason given by one reference that would motivate one of ordinary skill in the art at the time of the invention (hereinafter, "OOSA") to combine it with the teachings of the other

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reference. More importantly, Applicant submits that the Office has not provided any objective evidence showing why OOSA would be motivated to combine the teachings of the two references.

Duvall says nothing that would motivate OOSA to look towards Chipman and combine their teachings. Likewise, McCoy says nothing that would motivate OOSA to look towards Duvall and combine their teachings.

Accordingly, Applicant submits that OOSA would not be motivated to combine the VM file-access I/O addressing scheme of **Duvall** with the "dumb" terminal emulation of **McCoy**. Accordingly, Applicant asks the Office to withdraw its rejection of this claim.

Claims 30-33

These claims ultimately depend upon independent claim 29. As discussed above, claim 29 is allowable.

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Dependent Claims

In addition to its own merits, each dependent claim is allowable for the same reasons that its base claim is allowable. Applicant submits that the Office withdraw the rejection of each dependent claim where its base claim is allowable.

Conclusion

All pending claims are in condition for allowance. Applicant respectfully requests reconsideration and prompt issuance of the application. If any issues remain that prevent issuance of this application, the Office is urged to contact the undersigned attorney before issuing a subsequent Action.

Dated: 4-29-05

By:

Rasey C. Christie Reg. No. 40559 (509) 324-9256 x232 kasey@lechayes.com

Respectfully Submitted,

Serial No.: 09/847,535 Auy Docket No.: MSI-665us RESPONSE TO OFFICE ACTION DATED 11/30/2004